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September 19, 2005

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WRITER'S DIRECT NUMBER:

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 Art Unit 2823

Attn: Mail Stop AF

Re:

U.S. Utility Patent Application

Application No. 10/750,834; Filed: January 5, 2004

For: High Density Metal Capacitor Using Via Etch Stopping Layer as

Field Dielectric in Dual-Damascence Interconnect Process

Inventor:

Liming Tsau

Our Ref:

1875.0230001

Sir:

Transmitted herewith for appropriate action are the following documents:

- 1. Reply Under 37 C.F.R. § 1.116;
- 2. Declaration of Liming Tsau Under 37 C.F.R. § 1.131;
- 3. Redacted Information Disclosure Form and attachment (Exhibit A); and
- 4. Return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

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Commissioner for Patents September 19, 2005 Page 2

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Jason D. Eisenberg

Attorney for Applicant Registration No. 43,447

JDE/lvt Enclosures

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